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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10066603	FILING DATE 02/06/2002	CLASS 257	SUBCLASS	GAU 2844	EXAMINER
<b>**APPLICANTS:</b> Mori Kaoru; Matsumiya Masato;					
<b>**CONTINUING DATA VERIFIED:</b> <i>None</i>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> JAPAN 2001-102175 03/30/2001 <i>Yes</i>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials <i>[Signature]</i>		100353-00099			
<b>TITLE :</b> Semiconductor memory device with efficient and reliable redundancy processing <small>U.S. DEPT. OF COMM./PAT. &amp; TM.-PTO-438L (Rev. 12-94)</small>					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Primary Examiner PREPARED FOR ISSUE Application Examiner	
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